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(54) Dual phased-array payload concept

(57) A dual phased array payload (100) for use on-board a communications satellite is disclosed. The payload includes one or more phased array receive antennas (102-108) including numerous individual receiving elements distributed in a predetermined configuration. Each of the individual radiating elements is selectively adjustable in amplitude and phase to achieve scanning beams for receiving information transmitted from the ground in an uplink beam. The payload includes a packet switch (114) connected to the phased array receive antennas (102-108). The packet switch (114) includes a set of inputs and a set of outputs. The set of inputs are selectively connectable to the set of outputs. The payload (100) includes one or more phased array transmit antennas (120-126) connected to the packet switch (114). The phased array transmit antennas (120-126) include numerous individual radiating elements distributed in a predetermined configuration. Each of the individual radiating elements has a controllable amplitude and phase excitation used to electronically steer a downlink beam produced by the phased array transmit antennas (120-126). A payload computer (132) is connected to the packet switch (114). The payload computer

ter includes outputs that control the connection of the packet switch inputs to the packet switch outputs. The communications satellite may communicate with the ground, or with other satellites, using a beacon (128). The beacon (128) operates under control of the payload computer (132) to transmit and receive command, control, and status information to the ground.

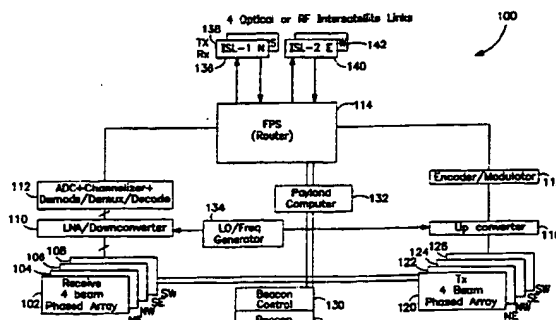


FIG. 1



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			TECHNICAL FIELDS SEARCHED (Int.Cl.7)
			H04B H01Q
The present search report has been drawn up for all claims			
Place of search MUNICH		Date of completion of the search 21 October 2002	Examiner Galli, P
<p>CATEGORY OF CITED DOCUMENTS</p> <p>X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document</p> <p>T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document</p>			

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